

S/N 10/612290

PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	James D. Patterson	Examiner:	Unknown
Serial No.:	10/612290	Group Art Unit:	2838
Filed:	June 30, 2003	Docket:	884.910US1
Title:	CROSS-OVER VOLTAGE LOCK FOR DIFFERENTIAL OUTPUT DRIVERS		
Assignee:	Intel Corporation	Customer No:	21186

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 19-0743 in order to have this Information Disclosure Statement considered.

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Serial No : 10/612290

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Title: CROSS-OVER VOLTAGE LOCK FOR DIFFERENTIAL OUTPUT DRIVERS

Assignee: Intel Corporation

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Dkt: 884.910US1 (INTEL)

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

JAMES D. PATTERSON

By his Representatives,

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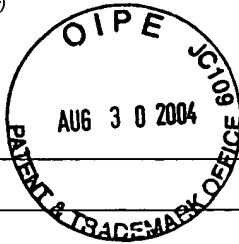
Date Aug 24, 2004 By Ann M. McCrackin  
Ann M. McCrackin  
Reg. No. 42,858

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 24 day of August, 2004.

Name KACIA LEE

Signature Kacia Lee

Substitute for form 1449A/PTO <b>INFORMATION DISCLOSURE                  STATEMENT BY APPLICANT</b> (Use as many sheets as necessary)	Complete if Known	
	Application Number	10/612290
	Filing Date	June 30, 2003
	First Named Inventor	Patterson, James
	Group Art Unit	2838
	Examiner Name	Unknown
Attorney Docket No: 884.910US1		



Sheet 1 of 1

US PATENT DOCUMENTS						
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
	US-6,566,926	05/20/2003	Patterson, James D.	327	206	06/25/2002

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	

EXAMINER

DATE CONSIDERED